

Enzo Tartaglione

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BIOGRAPHY

Enzo Tartaglione is a Full Professor at Télécom Paris (Institut Polytechnique de Paris), where he is also responsible for the group Multimedia, Hi!Paris associate member, member of the ELLIS society, Senior member of IEEE, Associate Editor of *IEEE Transactions on Neural Networks and Learning Systems* since 2024, of *Transactions on Machine Learning Research* since 2025, and of the *EURASIP Journal on Image and Video Processing* since 2025. He received the MS in Electronic Engineering at Politecnico di Torino and Politecnico di Milano in 2015, *cum laude*. The same year, he also received a *magna cum laude* MS in electrical and computer engineering at the University of Illinois at Chicago. In 2019 he obtained a Ph.D. in Physics at Politecnico di Torino, *cum laude*, with the thesis "From Statistical Physics to Algorithms in Deep Neural Systems" and in 2024 his HDR. He is an Area Chair in top AI venues and has been nominated outstanding reviewer three times in these. Besides, he was keynote speaker at PCS 2025 and a finalist for the 2025 Multimedia Rising Star Award. His principal interests include compression, sparsification, pruning of deep neural networks, computer vision, debiasing, 3D Gaussian Splatting, and regularization for deep learning.

ACADEMIC APPOINTMENTS

Télécom Paris, Institut Polytechnique de Paris, France

Responsible of the equipe Multimedia	Sep 2024 – now
Full Professor in Deep Learning and Efficient AI	Jan 2026 – now
Associate Professor in Deep Learning and Efficient AI	Jul 2024 – Dec 2025
Assistant Professor in Deep Learning and Efficient AI	Oct 2021 – Jun 2024
Hi!Paris Society associate member	Sep 2025 – now
Hi!Paris Society chair holder	Oct 2021 – Aug 2025

Università degli Studi di Torino, Italy

Post-Doctorate in Computer Vision and Pattern Recognition	Mar 2019 – Sep 2021
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VISITING PERIODS

Shanghai Jiao Tong University, Shanghai, China — Computer Vision course in the frame of the SPEIT international program	Mar 2026 – Apr 2026
MILA / École de Technologie Supérieure, Montréal, Canada — Research collaboration in model debiasing, privacy, and efficiency	Aug 2025 – Sep 2025
University of Genova, Genova, Italy — Research collaboration in model debiasing and fairness	Jul 2024 – Aug 2024

EDUCATION

Habilitation à diriger des recherches, Institut Polytechnique de Paris, France — <i>From model complexity reduction to feature engineering in deep learning: a regularization story</i>	04 Sep 2024
Ph.D. in Physics (<i>cum laude</i>), Politecnico di Torino, Italy — <i>From Statistical Physics to Algorithms in Deep Neural Systems</i>	Nov 2015 – Jul 2019
M.Sc. Embedded Systems (110/110 <i>cum laude</i>), Politecnico di Torino, Politecnico di Milano, and University of Illinois at Chicago	Sep 2013 – Jul 2015

EDITORIAL ACTIVITY

- **Associate Editor** for *IEEE Transactions on Neural Networks and Learning Systems* (2024 – now)
- **Action Editor** for *Transactions on Machine Learning Research* (2025 – now)
- **Associate Editor** for the *EURASIP Journal on Image and Video Processing* (2024 – now)

MEMBERSHIPS AND CONFERENCE ACTIVITY

- **IEEE Senior Member** (2024 – now); IEEE Member (2019 – 2024)
- **ELLIS Society Member** (2023 – now)
- **International Association for Pattern Recognition (IAPR) Member** (2021 – now)
- **Area Chair:** ICML (2026), ICLR (2026), NeurIPS (2026, 2025), ICME (2026), IJCNN (2026, 2025), ICASSP (2026)
- **Senior Program Committee:** IJCAI-ECAI (2026)
- **Session Chair:** ECML-PKDD (2024), ICIP (2022)
- **Conference Reviewer:** CVPR (2026, 2025, 2024, 2023, 2022, 2021), ICCV (2025, 2023, 2021), ECCV (2024, 2022), WACV (2026, 2025, 2024, 2023), AAAI (2026, 2025, 2024, 2023), ICLR (2025, 2024), ICML (2025, 2024), NeurIPS (2025, 2024, 2023), ACM MM (2025), AISTATS (2025), BMVC (2024, 2023), ECML-PKDD (2024), ICASSP (2025, 2023), ICME (2025, 2023)

GRANTS AND FUNDING SOURCES

- Hi!Paris internal chair, Project **GIFFAI** "Grounding Information Flow for Frugal AI". *Role: Principal Investigator.* (2025 – 2028)
- Project **BANERA** "Bias-Aware NEural aRchitecture seArch". *Role: Principal Investigator.* (2024 – 2028)
- Agence Nationale de la Recherche (ANR-TSIA) Project **SODA** "System On Chip Design leveraging Artificial Intelligence". *Role: Team Member.* (2023 – 2027)
- European Project **ELIAS** "European Lighthouse of AI for Sustainability". *Role: Task leader for "Reducing Energy Requirements of Computation".* EU grant n. 101120237. (2023 – 2027)
- Hi!Paris external chair, 3 years fixed-term assistant professor recruitment. *Role: Principal Investigator.* (2021 – 2024)

AWARDS AND HABILITATIONS

- French Full Professor habilitation (*Qualification au métier de Professeur*) for Computer Science (27) and Computer Science Engineering (61) — 2025
- Italian Associate Professor habilitation for Computer Science (B/1, II fascia) — 2025
- Finalist for the [Multimedia Rising Star Award 2025](#) — 2025
- Outstanding reviewer award: [CVPR 2025](#) (rank A*), [ICLR 2025](#) (rank A*), [CVPR 2021](#) (rank A*)
- Caianiello best paper award of the International Conference on Image Analysis and Processing — [Sparse Double Descent in Vision Transformers: real or phantom threat?](#) (2023)

- Italian National habilitation as Electronic Engineer — 2016

SELECTION OF RECENTLY ORGANIZED EVENTS

- Workshop organizer: [IP Paris – Hi! PARIS Computer Vision Workshop](#) — 2026
- Workshop co-organizer: [Rethinking AI: Efficiency, Frugality, and Sustainability](#), EurIPS 2025, Copenhagen, Denmark — 2025
- Workshop co-organizer: [2nd workshop on Fairness and ethics towards transparent AI: facing the chalLEnge through model Debiasing \(FAILED\)](#), ICCV 2025, Honolulu, USA — 2025
- Workshop co-organizer: [Fairness and ethics towards transparent AI: facing the chalLEnge through model Debiasing \(FAILED\)](#), ECCV 2024, Milan, Italy — 2024
- Workshop co-organizer: [Workshop on Green Foundation Models](#), ECCV 2024, Milan, Italy — 2024
- Workshop organizing chair: [Simplification, Compression, Efficiency and Frugality for Artificial Intelligence](#), ECML 2023 — 2023
- Special session chair: [Simplification, Compression and Efficiency with Neural Networks and Artificial Intelligence](#), ICIAP 2022, Bordeaux, France — 2022
- Tutorial organizing chair: [Pruning deep neural networks: towards efficient models on the edge](#), ICIAP 2022 — 2022

SELECTION OF RECENT INVITED TALKS

- Speaker for the Paris-Saclay Summit — 2026
- Seminar for the *Journal on Image and Video Processing* — 2026
- Keynote speaker at the 2025 Picture Coding Symposium (PCS) — 2025
- Speaker at the Global AI Summit: balancing innovation with social responsibility — 2025
- Hi!PARIS Meet Up! on the AI Act — 2025
- Adra-e European Convergence Summit — ADR facing Environmental issues — 2024

PH.D. EXAMINATION COMMITTEE

- **Tom BORDIN** — *Image semantic compression at extremely low bitrates*. Role: Thesis reviewer. Université de Rennes, France — Dec 2025
- **Miles EVERETT** — *Unlocking Scalability in Non-Iterative Capsule Networks*. Role: Thesis reviewer. University of Aberdeen, United Kingdom — May 2025
- **Tharsan SENTHIVEL** — *Towards more Efficient Transformers for Object Detection and Tracking*. Role: Thesis reviewer. Cergy Paris Université, France — Dec 2024
- **Farzad NIKFAM** — *Security and Privacy in Artificial Intelligence*. Role: Thesis reviewer. Politecnico di Torino, Italy — Sep 2024
- **Ali HASSAN** — *Parameter-Efficient Convolutional Neural Networks for Computer Vision Applications*. Role: Committee member. Mittuniversitetet, Sweden / Tampere University, Finland — Feb 2026

- **Yiqun LIU** — *Learning for new generation video coders*. Role: Committee member. Université de Rennes, France — Dec 2023